



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Software Application of
Craig HANSEN, et al.
Serial No.: 10/616,303
Filed: July 10, 2003
For: PROGRAMMABLE PROCESSOR AND METHOD WITH WIDE OPERATIONS

: Customer Number: 20277
: Confirmation Number: 4587
: Group Art Unit: 2183
: Examiner: Unknown

:

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop IDS
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

To prevent excessive duplication, only copies of references that are not already of record in a parent are being submitted herewith. The examiner is invited to contact the undersigned if any other copies are needed.

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Respectfully submitted,

MCDERMOTT, WILL & EMERY



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Date: December 19, 2003

INFORMATION DISCLOSURE
CITATION IN AN
APPLICATION

(PTO-1449)

ATTY. DOCKET NO.
43876-144SERIAL NO.
10/616,303APPLICANT
Craig HANSEN, et al.FILING DATE
July 10, 2003GROUP
2183

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ₂ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	US	4,785,393	11/15/1988	Chu et al.	
	US	4,814,976	03/21/1989	Craig C. Hansen, et al.	
	US	5,031,135	07/09/1991	Patel	
	US	5,280,598	01/1994	Osaki et al.	
	US	5,481,686	01/02/1996	Dockser	
	US	5,487,024	01/1996	Girardeau Jr.	
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	US	5,740,093	04/14/1998	Sharangpani	
	US	5,742,840	04/21/1998	Hansen et al.	
	US	5,768,546	06/1998	Kwon	
	US	5,898,849	04/27/1999	Tran	
	US	5,996,057	11/30/1999	Hunter L. Scales, III, et al.	
	US	6,041,404	03/21/2000	Patrice Roussel, et al.	
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FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number +-Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	
		IEEE Draft Standard for "Scalable Coherent Interface-Low-Voltage Differential Signal Specifications And Packet Encoding", IEEE Standards Department, P1596.3/D0.15 (March 1992)	
		IEEE Draft Standard for "High-Bandwidth Memory Interface Based on SCI Signaling Technology (RamLink)", IEEE Standards Department, Draft 1.25 IEEE P1596.4-199X (May 1995)	
		IBM, "The PowerPC Architecture: A Specification For A New Family of Risc Processors", 2nd Ed., Morgan Kaufmann Publishers, Inc., (1994).	
		Hewlett-Packard Co., "PA-RISC 1.1 Architecture and Instruction Set", Manual Part No. 09740-90039, (1990).	
		MIPS Computer Systems, Inc., "MIPS R4000 User's Manual", Mfg. Part No. M8-00040, (1990).	

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DATE CONSIDERED

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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.



Docket No.: 43876-144

PATENT

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				APPLICANT HANSEN, et al.		
				FILING DATE July 10, 2003	GROUP 2183	
U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	4,025,772	05/24/77	Constant			
	4,489,393	12/18/84	Kawahara, et al.			
	4,701,875	10/20/87	Konishi, et al.			
	4,727,505	02/23/88	Konishi, et al.			
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	4,969,118	11/06/90	Montoye, et al.			
	4,975,868	12/04/90	Freerksen			
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	5,201,056	04/06/93	Daniel, et al.			
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						Yes
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	Parallel Computers for Graphics Applications, Adam Levinthal, Pat Hanrahan, Mike Paquette, Jim Lawson, Pixar San Rafael, California, 1987					
	Organization of the Motorola 88110 Superscalar RISC Microprocessor, Keith Diefendorff and Michael Allen.					
	Microprocessor Report, Volume 7 Number 13, October 4, 1993, IBM Regains Performance Lead with Power2, Six Way Superscalar CPU in MCM Achieves 126 SPECint92.					
	IBM Creates PowerPC Processors for AS/400, Two New CPU's Implement 64-Bit Power PC with Extensions by Linley Gwennap, July 31, 1995.					
EXAMINER	DATE CONSIDERED					

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	5,423,051	06/06/95	Fuller, et al.			
	5,426,600	06/20/95	Nakagawa, et al.			
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	5,666,298	09/09/97	Peleg, et al.			
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						Yes	No
	0 474 246 A2	06/09/91	Japan				
	0 654 733 A1	05/07/94	Germany				

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	The Visual Instruction Set (VIS) in UltraSPAR TM , L. Kohn, G. Maturana, M. Tremblay, A. Prabhu, G. Zyner, May 3, 1995.
	Osborne McGraw-Hill, i860 TM Microprocessor Architecture, Neal Margulis, Foreword by Les Kohn
	A General-Purpose Array Processor for Seismic Processing, Nov-Dec., 1984, Volume 1, No. 3) Revisiting past digital signal processor technology, Don Shaver- Jan-Mar. 1998.
	Accelerating Multimedia with Enhanced Microprocessors, Ruby B. Lee, 1995.

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	5,721,892	02/24/98	Peleg, et al.			
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	5,757,432	05/26/98	Dulong, et al.			
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